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(71) Applicant (for all designated States except US): **KONINKLIJKE PHILIPS ELECTRONICS N.V.** [NL/NL];
Groenewoudseweg 1, NL-5621 BA Eindhoven (NL).

(72) Inventor; and

(75) Inventor/Applicant (for US only): **BRIAIRE, Joseph** [NL/BE]; c/o Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL).

(74) Agent: **ELEVELD, Koop, J.**; Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL).

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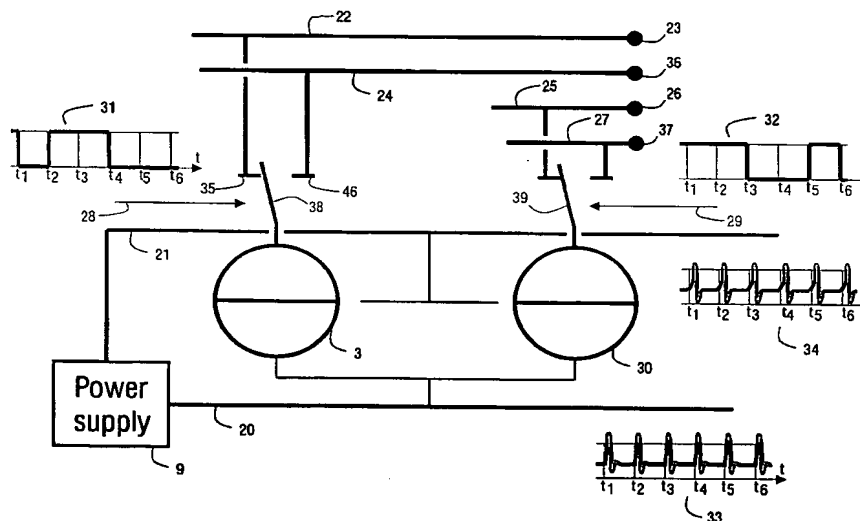
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(54) Title: A CURRENT STEERING D/A CONVERTER WITH REDUCED DYNAMIC NON-LINEARITIES



(57) Abstract: A digital to analog converter including a first current source (3) to which a first digital signal (28,31) is applied for conversion to an analog signal, wherein the first digital signal has a predetermined clock cycle. The digital to analog converter further comprising a second dummy current source (30) associated with the first current source to which a second digital signal (29,32) is applied. The second digital signal is derived from the first digital signal so that in any one clock cycle either the first or the second current source switches. This arrangement has the advantage that the dynamic behavior of the converter is not signal dependent, but dependent only on the clock cycle.

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